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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Satoshi SUGAHARA et al.

Group Art Unit: 2815

Application No.: 10/550,652

Examiner: E. LEE

Filed: December 7, 2005

Docket No.: 125426

For: RECONFIGURABLE LOGIC CIRCUIT USING A TRANSISTOR HAVING SPIN-DEPENDENT TRANSFER CHARACTERISTICS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

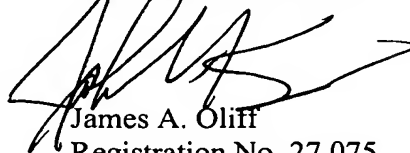
Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Further, an Information Disclosure Statement (IDS) and Form PTO-1449 were filed in the U.S. Patent and Trademark Office on April 8, 2008. As stated in the IDS, the references were cited in a counterpart foreign application. However, the English translation of the foreign Office action was not included. Attached is an English Translation of the foreign Office Action.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.

- ☒ 2. One or more reference cited herein was cited in the IDS filed on April 8, 2008 in a counterpart foreign application. An English language version of the foreign Office Action is attached for the Examiner's information. See Reference(s) 2 and 3 of the IDS filed on April 8.

Respectfully submitted,



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Date: July 17, 2008

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